

## Stress evolution due to EM in metal line confined: Model







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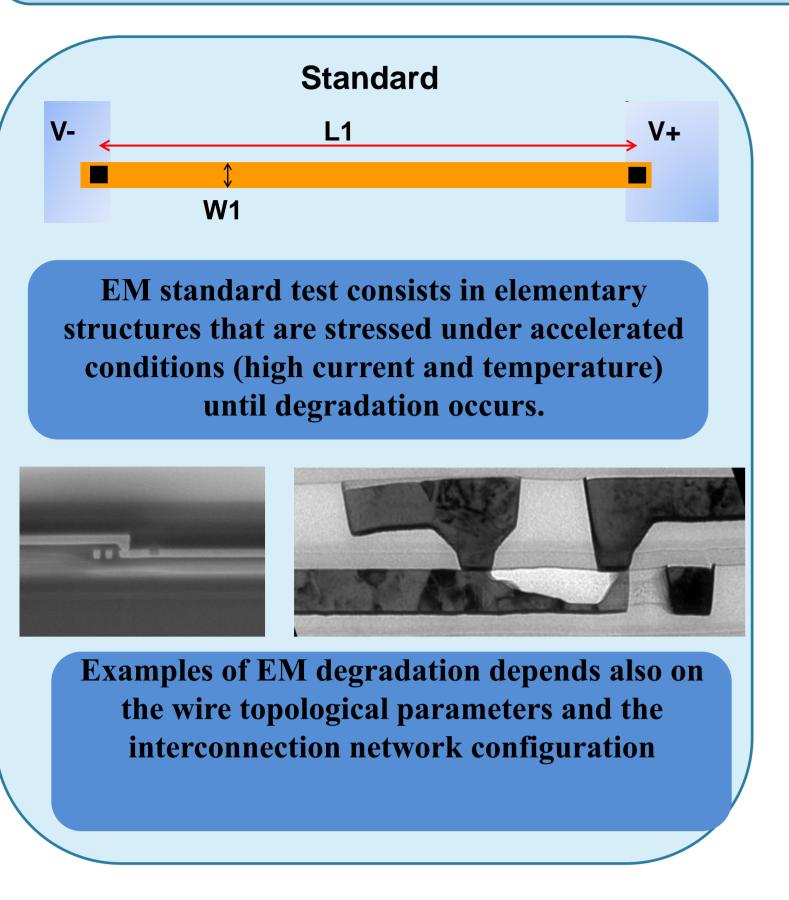
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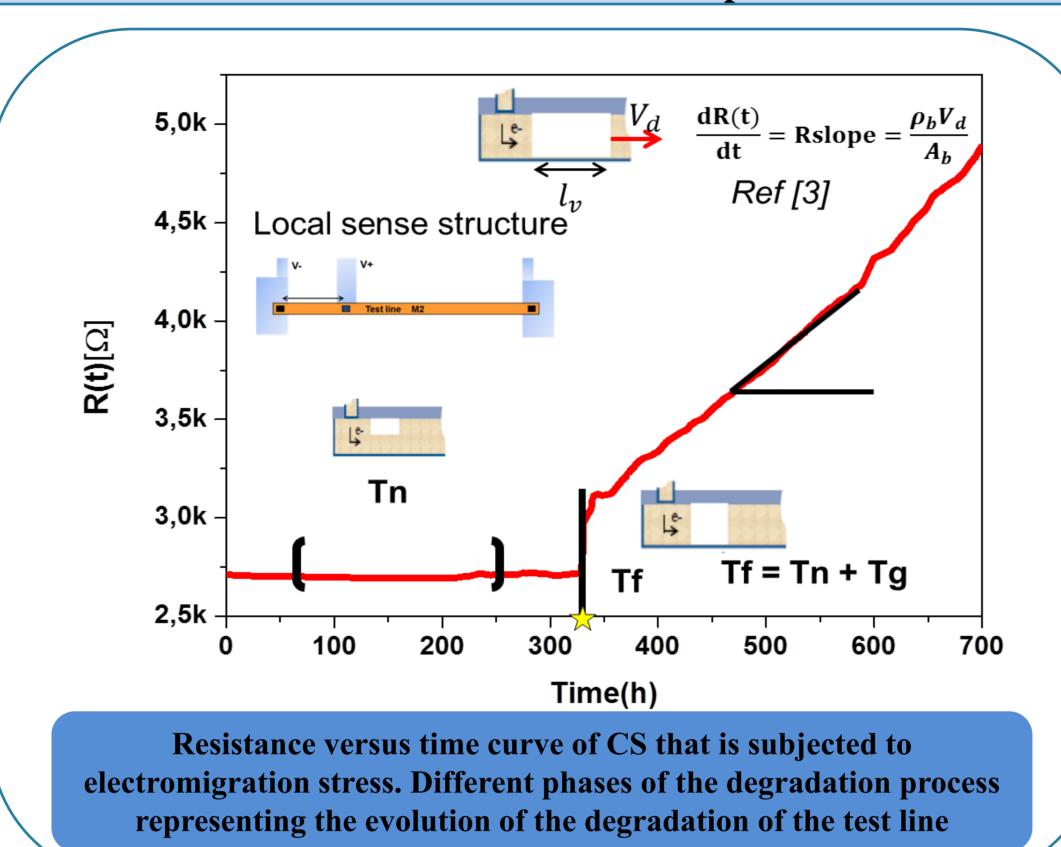
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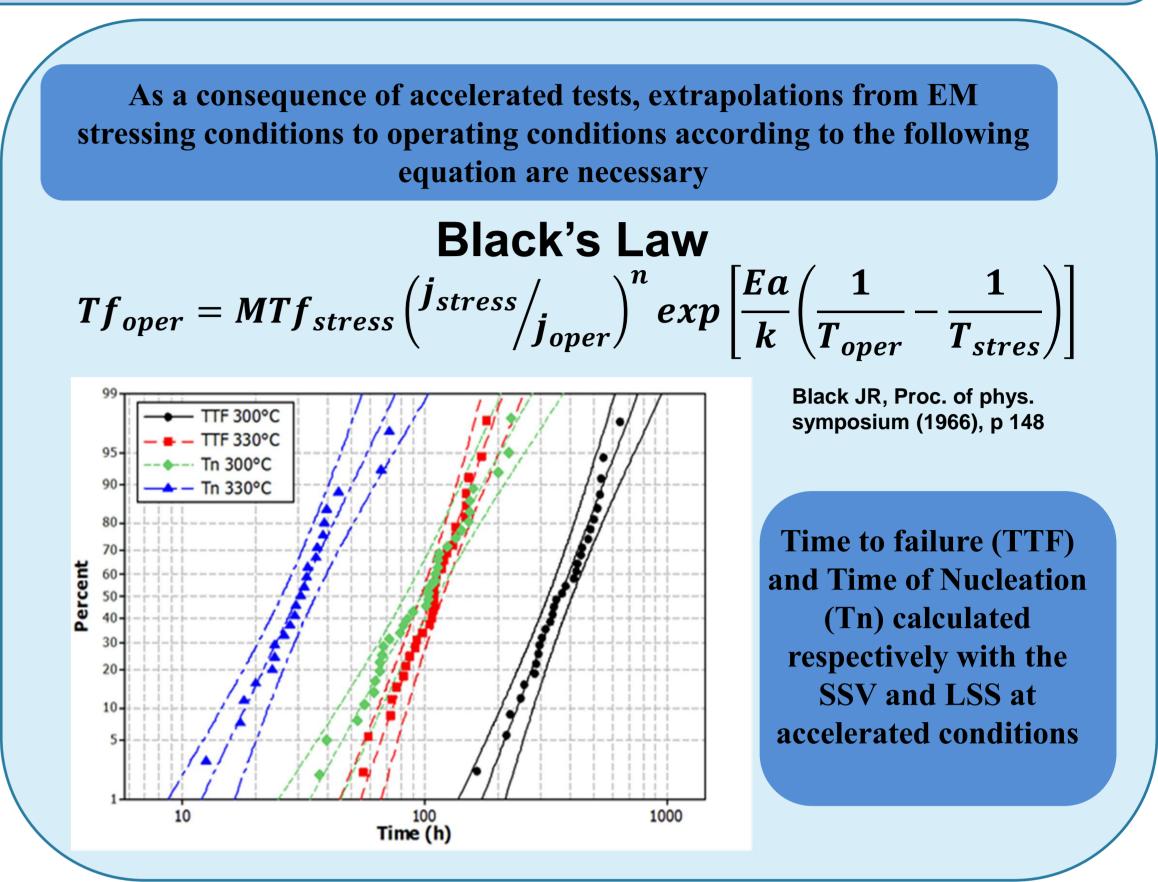
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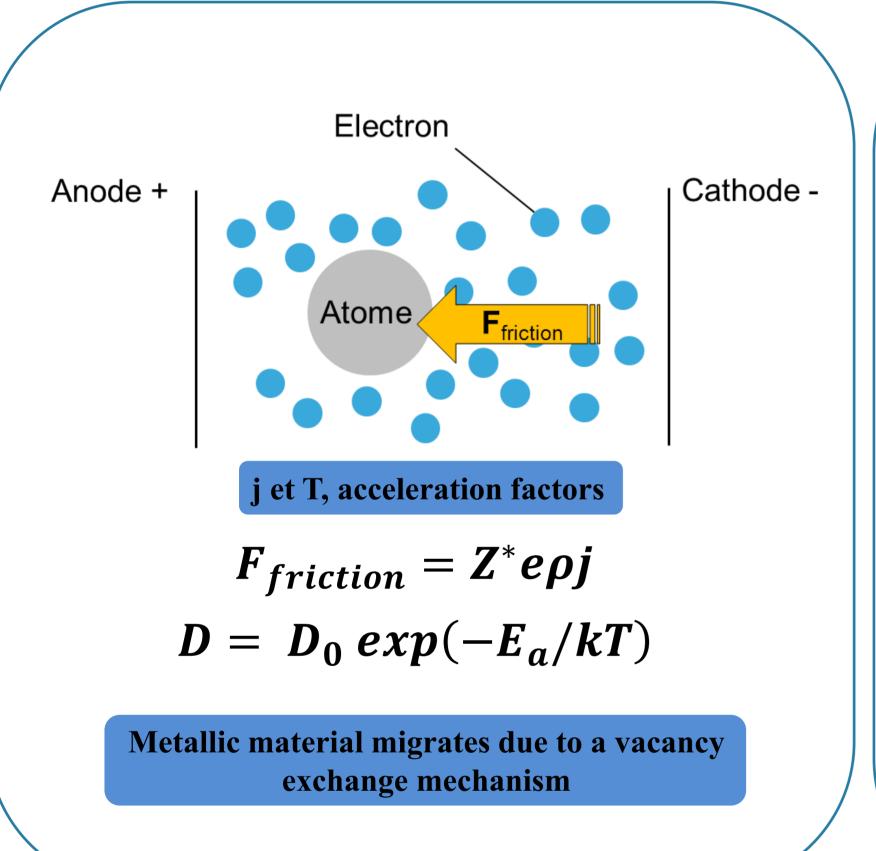
Electromigration (EM) is one of the main reliability failure mechanism of integrated circuit interconnects. The continuous scaling of the interconnect dimensions leads to higher operating current densities and temperatures, which accentuates the electromigration modelling becomes an important tool for explaining several experimental observations and can provide a stronger basis for design and fabrication of reliable metallization. The main challenge is the diversity of the relevant physical phenomena to take into account and the correlation with experiments.

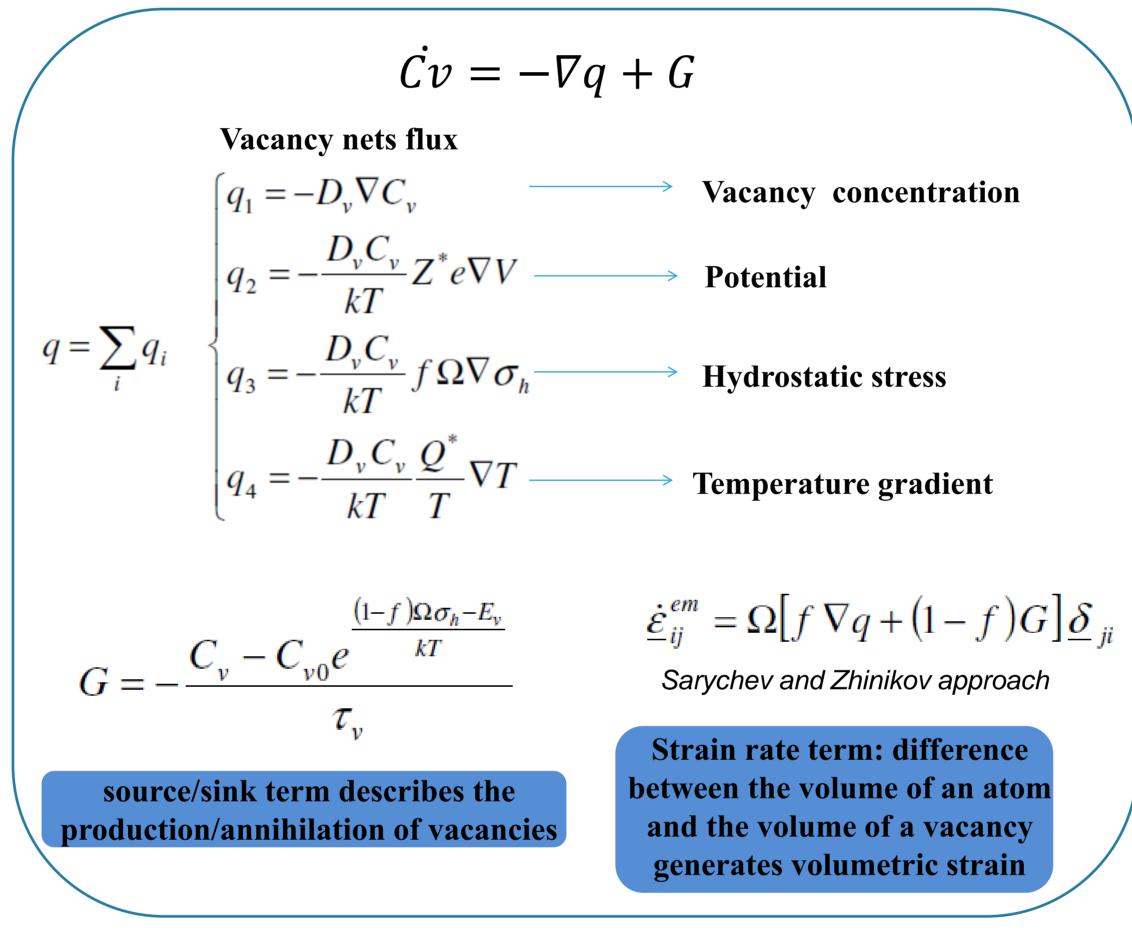


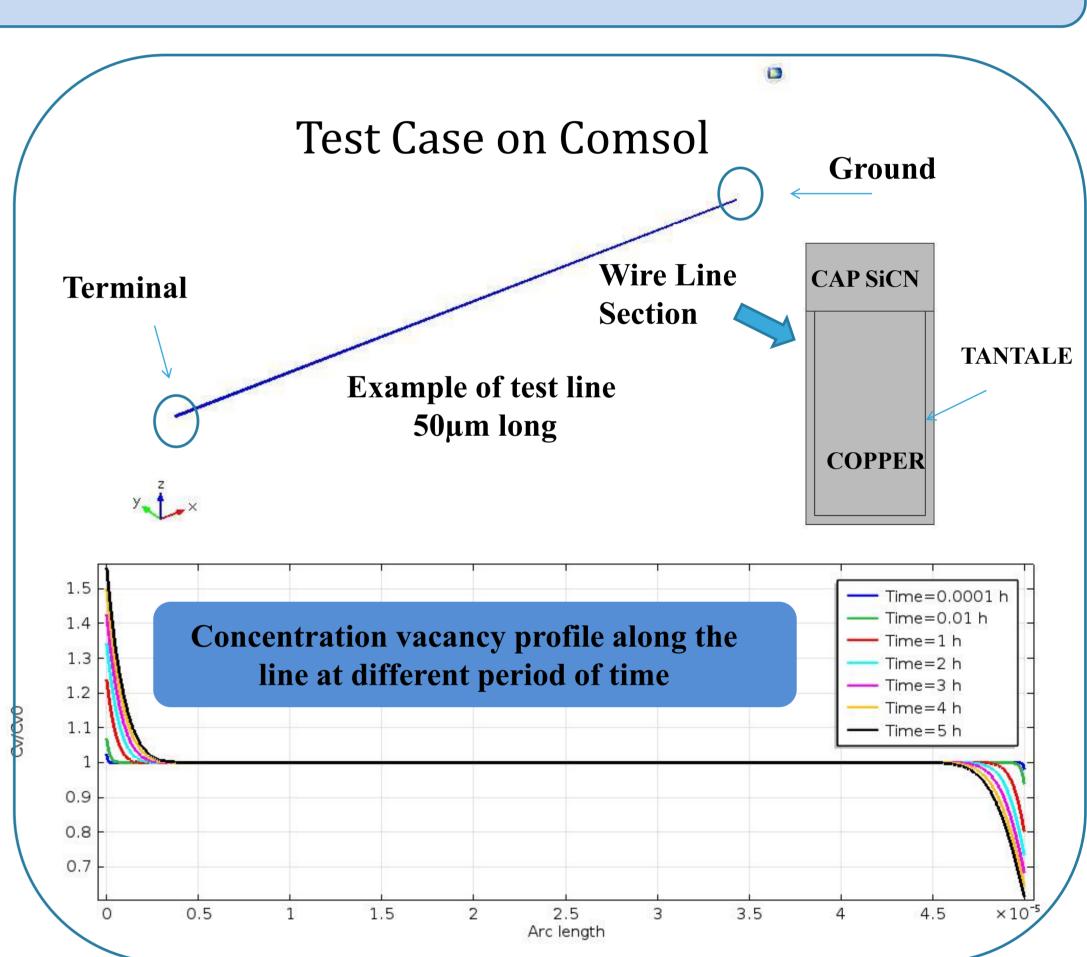




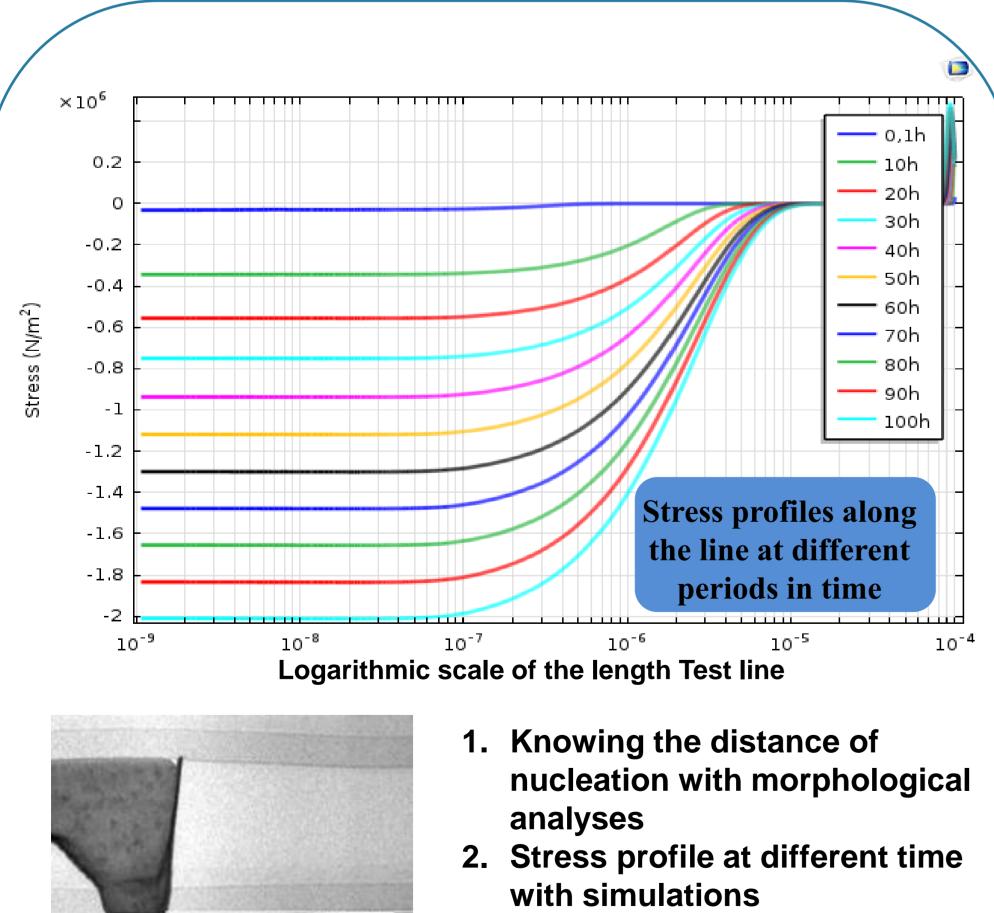
Electromigration modelling becomes an important tool for explaining several experimental observations and can provide a stronger basis for design and fabrication of reliable metallization. The main challenge is the diversity of the relevant physical phenomena to take into account. Indeed, the material transport is driven by the vacancy concentration, mechanical stress and temperature gradients, and electrical potential.







The degradation of the electrical resistance of interconnect segment can be derived from the solution of the kinetics equation<sup>[3]</sup> which describes the time evolution of vacancy's concentration (Cv) and stress ( $\sigma$ ). Then, the evolution of stress could be correlated with the experimental data with the purpose to find the critical stress necessary to nucleate a void.



Distance of the void

from the cathode

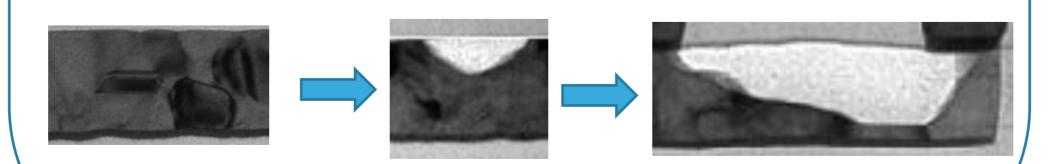
Allowed us to define a critical stress for the nucleation

## Conclusions

- Model of electromigration was reproduced simplified and adapted to the technology dual damascene Cu line embedded in low-k dielectric of the C028 technology process
- Time nucleation can be used now to calibrate existing models based on the critical mechanic stresses to have more realistic predictions
- Critical stress for the nucleation (calculated just approximately until now) can be evaluated more precisely

## Perspectives

Void growth model and correlations with experiments



## References

- [1] J.R. Lloyd, Black's law revisited: nucleation and growth in electromigration failure Microelectronics Reliability (2007), p 1468-1472
- [2] G. Marti et al., Study of EM void nucleation and mechanic relaxation effects, Microelectronic Engineering 54 (2014), p1692-1696
- [3] F. Cacho et al., Electromigration Induced Failure Mechanism: Multiphysics Model and Correlation with Experiments, IEEE-Euro SimE (2008)
- [4] L. Arnaud et al., Study of void formation in Cu interconnects using local sense and standard singlevia structure Proc. of ADMETA (2012), p 54-55
- [5] L. Doyen et al., Extensive analysis of resistance evolution due to electromigration induced degradation J. Appl. Phys. 104 (2008), 123521-1